

amended.

The amendments to the specification and drawings are made to correct various typographical errors and to ensure consistency between the specification and the drawings. It is respectfully submitted that the aforementioned amendments do not add any new matter within the meaning of 35 U.S.C. § 132.

The amendments to the specification at page 7, lines 29-31, are supported by the drawings of Figs. 5-7 and are intended to clarify what the figures show.

The amendment to the specification at page 10, line 26, is to correct a typographical error.

Oath/Declaration

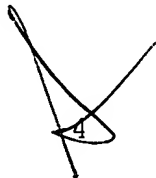
The oath/declaration was objected to. The Examiner stated:

The oath/declaration is defective. A new oath or declaration in compliance with 37 CFR 1.67(a) identifying this application by application number and filing date is required.

The oath or declaration is defective because: It does not state that the person making the oath or declaration believes the named inventor or inventors to be the original and first inventor or inventors of the subject matter which is claimed and for which a patent is sought.

RESPONSE

A new oath/declaration is herewith submitted which obviates the Examiner's objection.

A handwritten mark, possibly a signature or initials, consisting of a large 'V' shape with a horizontal line and a small circle at the base.

Accordingly, reconsideration and withdrawal of the objection is respectfully requested.

Drawings

The drawings were objected to. The Examiner stated:

The drawings are objected to because in figure 4B, the signal "rasatv<x>" should read --casatv<x>-- and in figure 5, the reference "60" as described in the specification is missing.

RESPONSE

Subject to the Examiner's approval, a Letter to the Draftsperson is herewith submitted proposing corrections to the drawings of Figs. 4B and 5 which obviate the Examiner's objections. Additionally, and subject to the Examiner's approval, a further correction to the drawing of Fig. 6 is likewise herewith proposed.

The proposed correction to the drawing of Fig. 4B corrects "rasatv<x>" to --casatv<x>-- to obviate the Examiner's objection. Support for the proposed correction is found at page 7, line 18, of the specification.

The proposed correction to the drawing of Fig. 5 obviates the Examiner's objection. Support for the proposed correction is found at page 7, lines 29-31, of the specification.

The proposed corrections to the drawing of Fig. 6 are to correct typographical errors relating to the first and second transmission gates and to identify those aspects of Fig. 6 which

comprise element 60. Support for adding reference numeral --60-- is found at page 7, lines 29-31, of the specification. Support for changing existing reference numeral "60" to --61-- is found at page 7, line 31, through page 8, line 1, of the specification. Support for changing existing reference numeral "61" to --62-- is found at page 8, lines 6-8, of the specification.

It is respectfully submitted that the proposed corrections add no new matter within the meaning of 35 U.S.C. § 132.

Accordingly, reconsideration and withdrawal of the objections is respectfully requested.

Rejection under 35 U.S.C. § 102

The Examiner rejected claims 1 under 35 U.S.C. § 102(e).

The Examiner stated:

Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by U.S. Patent 5,959,930 issued to Sakurai.

Sakurai discloses a multi-bank testing for a synchronous DRAM comprising: a row address strobe generating unit (inherent in the DRAM as a means for activating a word line), a column address strobe generating unit (inherent in the DRAM as a means for selecting a column to be connected to bit lines to local data bit lines), input/output sense amplifiers (included in each memory bank of Fig. 26), a transmission gate unit shown as gates GSs for controlling transmission of data to global read data bus lines shown as RDs, and an input/output comparing unit labeled as references 40s for comprising data from the input/output sense amplifiers.

RESPONSE

The present invention, as claimed in claim 1, relates to a semiconductor memory device including a row address strobe generating unit for generating a signal adapted to simultaneously enable all of word lines having the same row address of each bank, a column address strobe generating unit for generating a signal adapted to enable transistors respectively adapted to simultaneously couple bit lines having the same column address of each bank, a transmission gate unit, and an input/output comparing unit.

Claim 1 has been amended to further distinguish the claimed invention from the prior art and, as amended, is asserted to now be patentable over the cited prior art.

Claim 1 now claims that a row address strobe generating unit generates a signal adapted to simultaneously enable all of word lines having a same row address of each bank to transmit data from cells to bit line sense amplifiers in each bank of said synchronous DRAM; and that a column address strobe generating unit generates a signal adapted to enable transistors respectively adapted to simultaneously couple bit lines having a same column address of each bank and carrying data, amplified by said bit line sense amplifiers, to local data bus lines.

The Sakurai patent relates to a semiconductor memory device including a plurality of banks capable of being driven

independently, a plurality of memory cells select units for performing an operation for selecting a memory cell in a corresponding bank when activated, and a control unit for simultaneously activating the memory cell select units which are provided for a predetermined number of one or more banks among the plurality of memory cell select units.

It is respectfully submitted that the present invention as claimed in claim 1 is patentable over the Sakurai '930 patent in that the row address strobe generating unit and the column address strobe generating unit provide for simultaneously enabling each of the word lines and bit lines of the plurality of banks having the same addresses.

Accordingly, reconsideration and withdrawal of the rejection is respectfully requested.

ALLOWABLE SUBJECT MATTER

It is noted that the Examiner stated that claims 2-8 would be allowable if rewritten in independent form to include all the limitations of the base claim and any intervening claims. This acknowledgment of patentable subject matter is noted with appreciation.

MISCELLANEOUS

It is noted that the Examiner acknowledged the claim for

foreign priority and that all priority documents have been received.

CONCLUSION

In light of the foregoing, Applicant submits that the application is now in condition for allowance. If the Examiner believes the application is not in condition for allowance, Applicant respectfully requests that the Examiner contact the undersigned attorney if it is believed that such contact will expedite the prosecution of the application.

Respectfully submitted,

NATH & ASSOCIATES PLLC

Date: May 24, 2000



Gary M. Nath
Registration No. 26,965
Harold L. Novick
Registration No. 26,011

NATH & ASSOCIATES PLLC
1030 Fifteenth Street, N.W.
Sixth Floor
Washington, DC 20005
(202) 775-8383

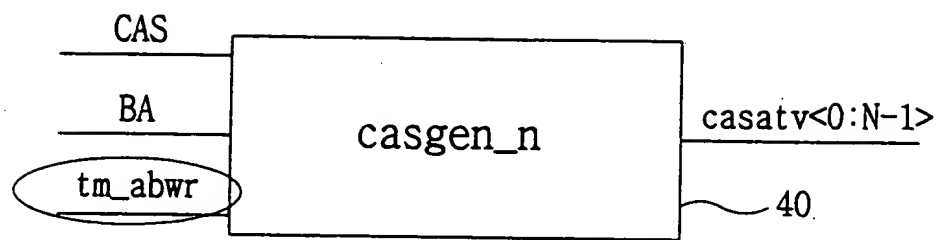


Fig.4A

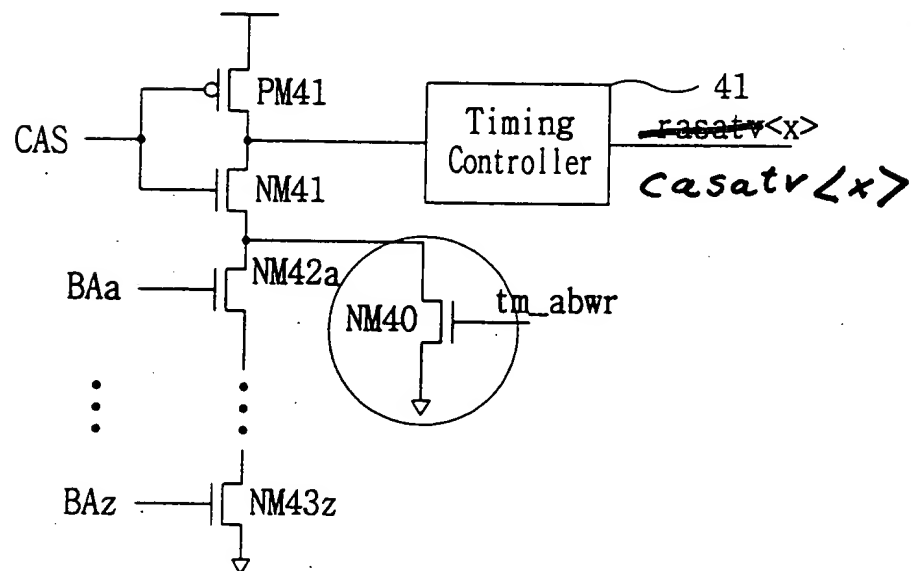


Fig. ~~fig.~~ 4B

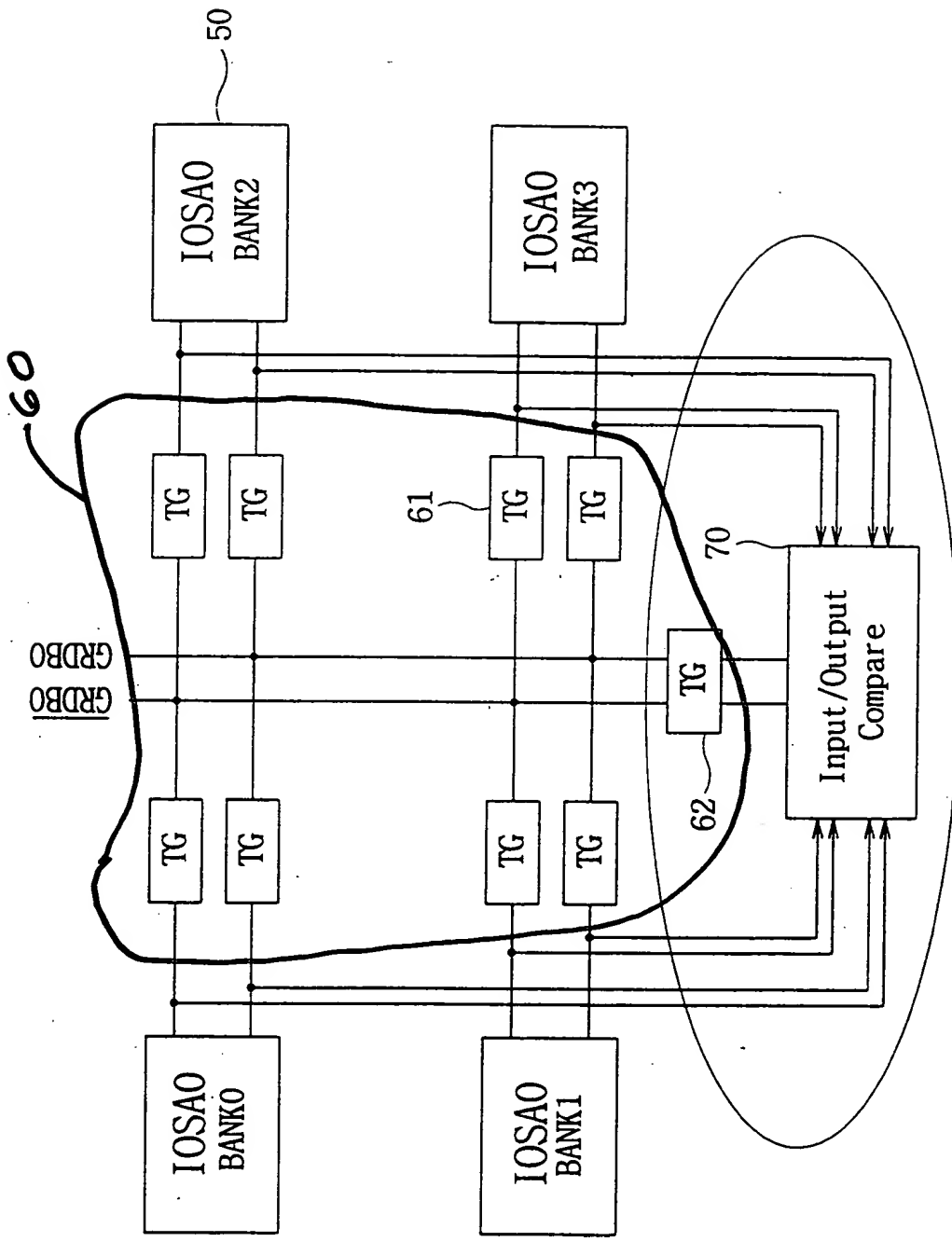


Fig. 5

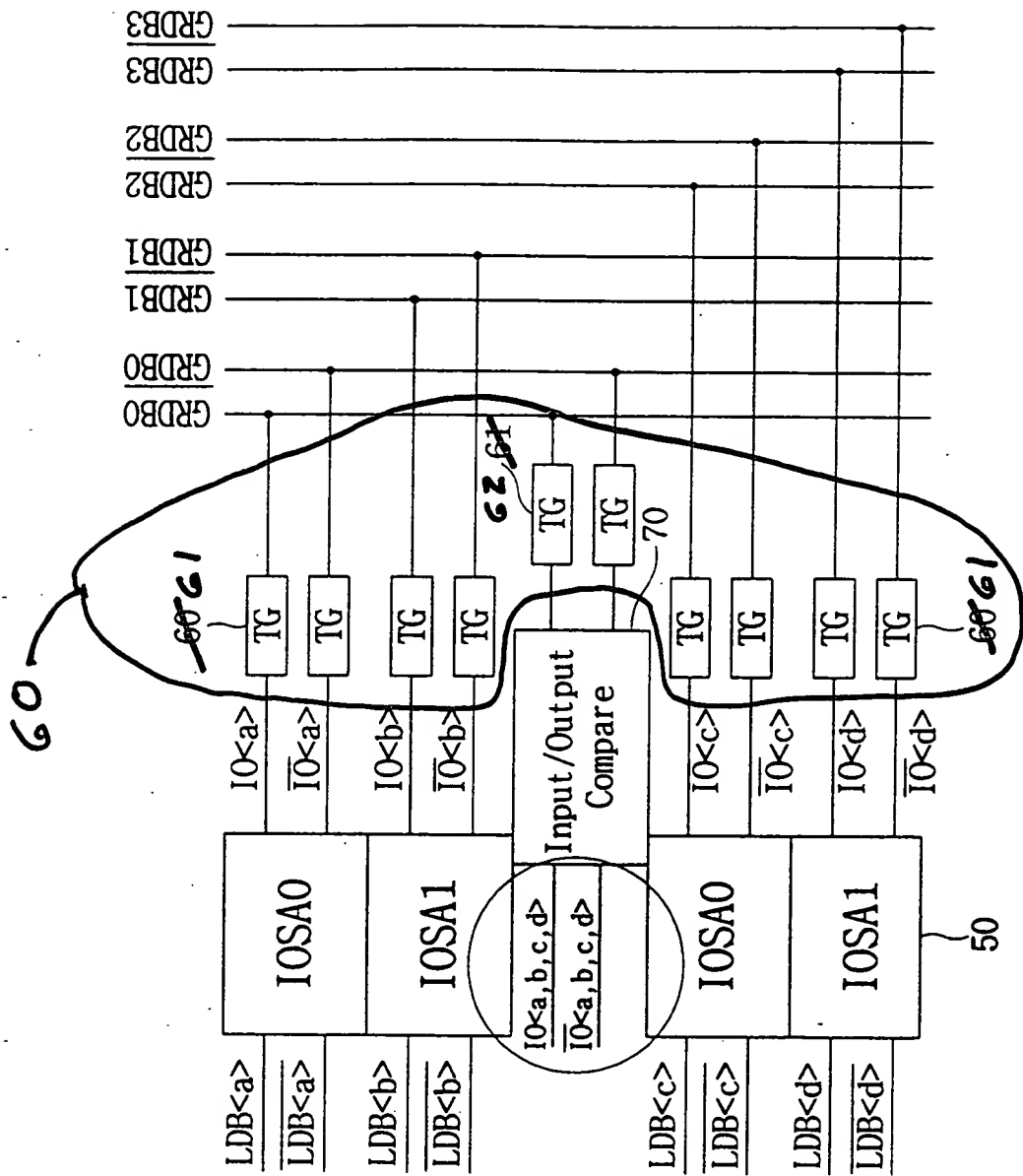


Fig. 6